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**Patent** 

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### In the United States Patent and Trademark Office

Date: October 4, 2005

In re Application of: Pille, et al.

Filed: 10/18/2004

For: Power Saving by Disabling Cyclic Bit Redundancy

Serial Number: 10/711,982

Confirmation Number: 5981

Art Unit: 2827

Examiner: Le, Thong Quoc

### REPLY AND AMENDMENT UNDER 37 C.F.R. §1.111

Commissioner of Patents & Trademarks Alexandria, VA 22313

Sir:

This is in response to a Notice of Non-Compliant Amendment mailed on September 29, 2005, which is due for response by October 29, 2005. Any fees required in entering this response may be charged to Applicant's deposit account, 09-0456.

It is respectfully requested that this Amendment be entered in the above referenced application and reconsideration of the application in view of these comments be made. No new matter has been included. The application should be amended as follows:

DE920030003US1 SN 10/711,982

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